

# DRAWING

Fig. 1 -- Signal Flow Driven Analog/RF Circuit Physical Synthesis Technique Flow Chart

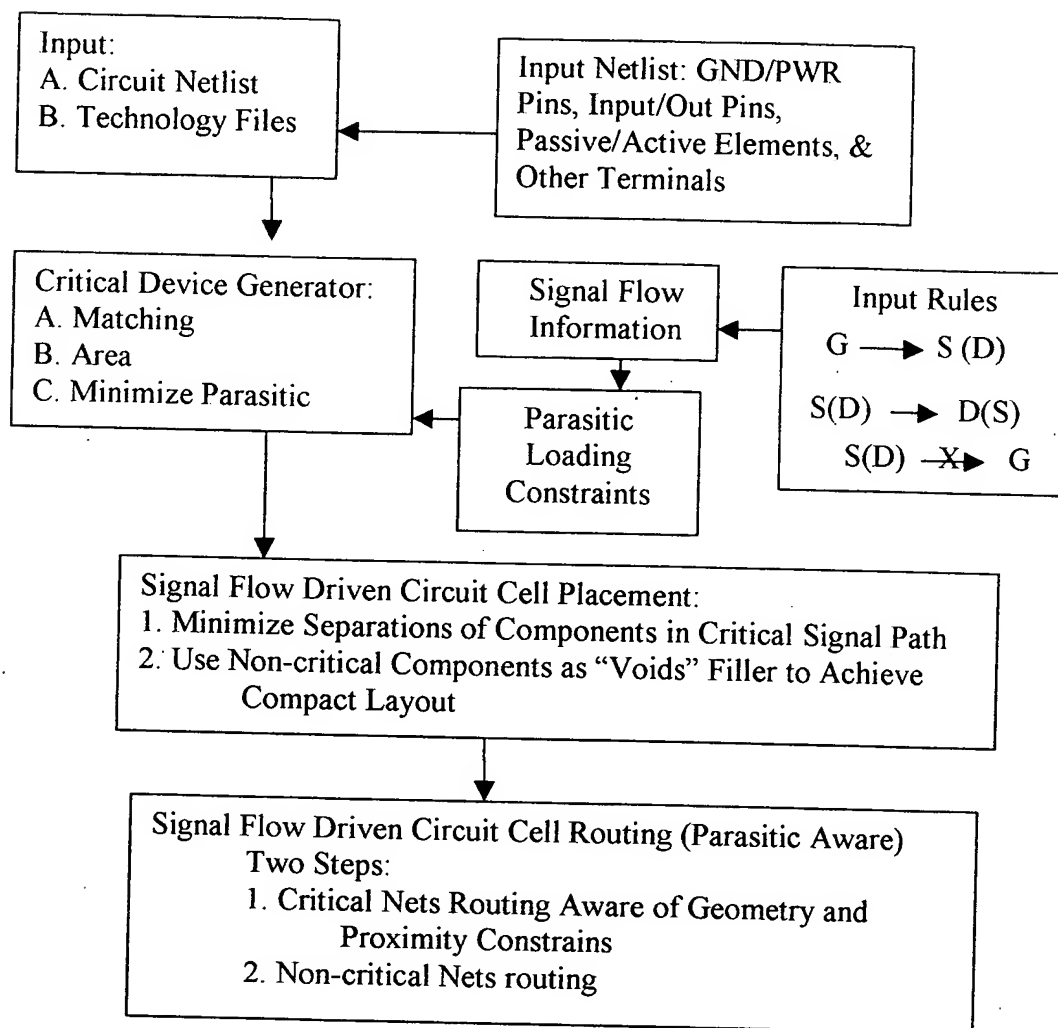


Fig. 2 -- Mean of Circuit Performance Assurance, Mean of Physical Layout Floor Planning, and Mean of Incorporating an In-situ Parasitic Extraction Process Flow Chart

